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What is claimed is:

1. A program guide data processing apparatus comprising:

an SCID_filter section including a plurality of SCIDs for receiving A/V data provided in an A/V signal format;

a frame_filter section, composed of a plurality of headers, for being mapped on the SCID-filter section in a multi-to-multi correspondence so that at least one of the headers corresponds to the plurality of SCIDs; and

a memory section for forming a buffer for each of the SCIDs, and storing program guide data in the unit of a frame.

2. The apparatus as claimed in claim 1, wherein the respective header in the frame_filter section comprises:

a frame_header section for representing an inherent value of the corresponding header;

an SCID_number section having at least one SCID set in the respective frame_header section; and

a mask section for selecting a mask of the corresponding header.

- 3. The apparatus as claimed in claim 2, wherein the SCID_number section represents an order set in the SCID_filter, that is a register number where the corresponding SCID is stored.
- 4. The apparatus as claimed in claim 1, wherein the memory section includes start and end address regions of the buffer where the program guide data is stored, a matched SCID_number section region, and a matched frame_header section region.

- 5. The apparatus as claimed in claim 4, wherein a size of the memory section is '(the maximum number of SCIDs corresponding to one header)*(the size of the buffer set by a user: the minimum value whereby the buffer is not in full)'.
- 6. The apparatus as claimed in claim 5, wherein when APG data is transmitted to the buffer, the SCID that matches the frame and the register number of the frame_header are also transmitted along with a frame start point, and a frame end point.
 - 7. The apparatus as claimed in claim 1, wherein the respective SCID in the SCID filter section comprises:

an identifier (ID) for representing an inherent value of the corresponding SCID; and

a bit map that matches at least one frame header.

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